## PARALLEL PORT PIN-OUTS:

|    | <u> </u>         |    |                                  |
|----|------------------|----|----------------------------------|
| 1  | STROBE*          | 2  | Twisted Pair Ground (STROBE*)    |
| 3  | DATA 1           | 4  | Twisted Pair Ground (Data Bit 1) |
| 5  | DATA 2           | 6  | Twisted Pair Ground (Data Bit 2) |
| 7  | DATA 3           | 8  | Twisted Pair Ground (Data Bit 2) |
| 9  | DATA 4           | 10 | Twisted Pair Ground (Data Bit 2) |
| 11 | DATA 5           | 12 | Twisted Pair Ground (Data Bit 2) |
| 13 | DATA 6           | 14 | Twisted Pair Ground (Data Bit 2) |
| 15 | DATA 7           | 16 | Twisted Pair Ground (Data Bit 2) |
| 17 | DATA 8           | 18 | Twisted Pair Ground (Data Bit 2) |
| 19 | ACK*             | 20 | Twisted Pair Ground (ACKNLG*)    |
| 21 | BUSY             | 22 | Twisted Pair Ground (BUSY)       |
| 23 | Ground           | 24 | Twisted Pair Ground (DEMAND)     |
| 25 | ON-LINE          | 26 | NC                               |
| 27 | Signal Ground    | 28 | Always Logic 1                   |
| 29 | NC               | 30 | Ground                           |
| 31 | Signal Ground    | 32 | Pin 32 Jumpered to Pin 34        |
| 33 | Chassis Ground   | 34 | For Sensing Printer Connected    |
| 35 | + 5V             | 36 | DEMAND                           |
| 37 | + 17V (Not used) | 38 | NC                               |
| 39 | + 17V (Not used) | 40 | NC                               |
| Ĺ  | <u> </u>         |    | İ                                |

(smm 08/04/93)