BIT-MAPPED REGISTER

S13 is a bit-mapped register. The supported register bits are defined in the table below.

A bit-mapped register provides some useful information and may be accessed through your program. However, do not use this register to control the modem. WRITING TO A BIT-MAPPED REGISTER MAY PRODUCE UNPREDICTABLE RESULTS.

BIT	CONDITION	FUNCTION
0		Undefined
1		Undefined
2	0	Parity disabled Parity enabled
3	0 1	Odd parity Even parity
4	0	7 data bits 8 data bits
5		Undefined
6		Undefined
7		Undefined

(css 07/28/93)